



10/723382

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Munns	Examiner:	Unknown
Serial No.:	10/723382	Group Art Unit:	2811
Filed:	November 25, 2003	Docket No.:	3165.41USU1
Customer No.	23552	Confirmation No.:	8003
Title:	SUPER LATTICE MODIFICATION OF OVERLYING TRANSISTOR		

CERTIFICATE UNDER 37 CFR 1.10:

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I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to Mail Stop Missing Parts, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

By: Teresa Anderson

Name: Teresa Anderson

**PRELIMINARY AMENDMENT**

Mail Stop Missing Parts  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Before examination of this application, please make the following amendments:

**Amendments to the Specification** are presented on page 2 of this paper.

**Remarks** are presented on page 3 of this paper.